IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) An electronic device, comprising: ~~(301) in which acceleration of data processing operations is provided, which device comprises a secure execution environment (304) to which access is restricted, and which device further comprises:~~

an accelerator ~~(311)~~ for accelerating cryptographic data processing operations, which accelerator is arranged with:

a first logical interface over which data to be processed is provided, and

a secure second logical interface over which cryptographic keys employed in ~~the operation of~~ processing ~~said~~ data is provided, wherein the first logical interface and the secure second logical interface share a same physical interface, and

_____ a configuration register arranged to indicate to the accelerator whether secure mode or normal mode is set by a processor arranged in the device.

2. (Cancelled).

3. (Cancelled)

4. (Currently Amended) The device ~~(301)~~ according to claim 3 1, wherein the configuration register ~~(313)~~ further is arranged such that it may be set in one of a plurality of possible encryption modes, the accelerator ~~(311)~~ being arranged to operate in the encryption mode set in the register.

5. (Currently Amended) The device ~~(401)~~ according to claim 1, wherein the accelerator ~~(411)~~ is arranged such that the first logical interface ~~(412)~~ and the secure second logical interface ~~(414)~~ are provided via respective physical interfaces.

6. (Currently Amended) The device ~~(301)~~ according to claim 1, wherein the first logical interface ~~(312)~~ of the accelerator is arranged such that it is accessible by any

application, while the secure second logical interface (312) of the accelerator is arranged such that it is accessible by protected applications only.

7. (Currently Amended) The device (301) according to claim 6, further being arranged such that protected applications may prevent other applications from accessing the accelerator (311).

8. (Currently Amended) The device (301) according to claim 6, wherein protected applications are applications which are allowed to execute in the secure execution environment (304).

9. (Currently Amended) The device (101) according to claim 1, further comprising:
        storage circuitry (105, 106, 107) arranged with at least one storage area in which protected data relating to device security are located, and wherein
        a processor (103) is arranged such that it may be set in one of at least two different operating modes; and, the device being further arranged such that:
        the processor is given access to said storage area, in which said protected data are located, when a secure processor operating mode is set,
        the processor is denied access to said storage area when a normal processor operating mode is set; and
        the processor is capable of accessing the secure second logical interface (312) of the accelerator (311), when the secure processor operating mode is set.

10. (Currently Amended) The device according to claim 9, wherein the processor (103) further is arranged such that protected applications control the processor operation mode.

11. (Currently Amended) A mobile communication terminal (300) comprising a device (301) according to claim 1.

12. (Currently Amended) A device (311) for acceleration of data processing operations, which device comprises:

a first logical interface (312) over which data to be processed is provided;
and

a secure second logical interface (313) over which cryptographic keys
employed in processing said data is provided.

13. (Currently Amended) The device (311) according to claim 12, wherein the
device is arranged such that the first logical interface and the secure second logical
interface share a same physical interface (312).

14. (Currently Amended) The device (311) according to claim 13, wherein the
device further comprises:

a configuration register (313) arranged to indicate to the device whether
secure mode or normal mode is set.